

Appl. No. 10/826,263

**Listing of the Claims:**

The text of all pending claims, (including withdrawn claims) is set forth below. Cancelled and not entered claims are indicated with claim number and status only. The claims as listed below show added text with underlining and deleted text with strikethrough. The status of each claim is indicated with one of (original), (currently amended), (cancelled), (withdrawn), (new), (previously presented), or (not entered). Please CANCEL claims 1-5 and 7-23 and AMEND claims 6 in accordance with the following:

1-5. (CANCELED)

6. (PREVIOUSLY PRESENTED) A source having an impedance and connected to a load, the source comprising:

a detection circuit to determine whether a current flow through the impedance is load-induced or source-induced;

a processing circuit to perform an operation based upon whether the current flow is load-induced or source-induced; wherein the source, the DC offset elimination circuit, and the output impedance circuits are not included and the remainder of the system is a measuring instrument further comprising an output indicator which indicates whether the current flow is source-induced or load-induced.

7-23. (CANCELED)